

1. Name

Neda Khavari, Lecturer, Part time

2. Education

2009 M.S. Engineering Management, California State University, Northridge
2006 M.S. Electrical Engineering, California State University, Northridge
2002 B.S. Melly University, Tehran, Electrical Engineering

3. Academic Experience

2006-2018 Part time lecturer ,Electrical and Computer Department, California State University,Northridge

4. Non-Academic Experience

2018-Present Logitech, Senior Firmware Engineer, Full Time
2016-2018 Baltic Latvin Universal Electronics(Blue Microphones),Senior Firmware Engineer,Full Time.
2013-2016 Universal Switching Corporation, Software Engineer, Full Time.
2012-2012 Gridtest Systems, Electronic Engineer Lab Technician Part Time.
2005-2006 CSUN, Electrical Engineer, Reasearch on FPGA Dynamic Burn-in test for Military and Aerospace Application
CSUN, Electrical Engineer, Research on FPGA Synthesis Tools Trade Offs Using VHDL.
2005 CSUN (Supported by Boeing Co.), Electrical Engineer/Digital, Performed a survey of RadHard /Rad Tolerant FPGAs for Military & Space Applications and their tradeoffs
2004-2005 CSUN, Electrical Engineer, , Design of a small experimental autonomous helicopter (Unmanned Aerial Vehicle)
2000-2002 Electronic Process, Electrical Engineer /Digital Design and develop of Multi court Scoreboard, and Single Court Scoreboards hardware and software in 8051 assembler on an embedded processor to Display game and set information for each court as well as overall team scores for home and guests.
1999-2002 T plastic, Electrical Test Engineer

5. Certifications or Professional Registrations

None.

6. Current Membership in Professional Organization

2010 Chair, IEEE Women in Engineering (San Fernando Valley)

7. Honors and Awards

2006 & 2009 Awards from Association of Professors and Scholars of Iranian Heritage for Recognition of Achievement in Graduate Level Education.
2009 Recognized as an Honor Student in Engineering Management, Department of Manufacturing Systems & Engineering Management, California State University Northridge

8. Service Activities

2006-2018	Member of Faculty at CSUN.
2005-2012	Research and study on Embedded system, hardware and software. Program Motorola and Arm Microcontrollers using Assembly language and C.
2005- 2006	Worked on FPGA Dynamic Burn-in test for Military and Aerospace Application Project
2005	Performed a survey of RadHard /Rad Tolerant FPGAs for Military & Space Applications and their tradeoffs (Supported by Boeing Co.)

9. Principal Publications

None.

10. Professional Development Activities

None.