

1. Name

Shahnam Mirzaei, Assistant Professor, Full time

2. Education

1993 B.S. Electrical Engineering, University of Tehran, Iran

1999 M.S. Electrical and Computer Engineering, California State University, Northridge, USA

2010 Ph.D. Electrical Engineering, University California, Santa Barbara, USA

3. Academic Experience

2013-present Assistant Professor, ECE Department, CSUN

2003-2013 Lecturer, CSUN

2007-2010 Research Assistant, UC Santa Barbara

2006-2007 Teaching Assistant, UC Santa Barbara

4. Non- Academic Experience

2010-2013 R&D Engineer, Exelis (Now Harris), Van Nuys, CA

2002-2004 Regional Field Applications Engineer (Generalist), Nu Horizons Electronics, Los Angeles, CA

1999-2002 Regional Field Applications Engineer (Xilinx FPGA Specialist), Nu Horizons Electronics and Xilinx, Los Angeles, CA

5. Certificates or Professional Registrations

None.

6. Current Membership in Professional Organizations

2014-present IEEE

7. Honors and Awards

2014 59th Annual National Engineers Outstanding Engineering Achievement Merit Award
Universal City, California

2012 Radar Systems, Internal Secret Patent Recognition on efficient long-data DFT
implementation on FPGA, Exelis, Van Nuys, California

2011 Radar Systems Spot Award Achievement Certificate in recognition of superior individual
effort for developing radar algorithms in radar digital receiver board, Exelis, Van Nuys,
California

2010 University of California, Santa Barbara, Electrical and Computer Engineering
Department Fellowship Award

1999 California State University, Northridge, Second prize of CSUN contest for Master's
projects

8. Service Activities

2013-present Advising undergraduate student projects, and serving in various committees,
Member of Technical Program Committee for many journals (IEEE Transactions on VLSI
Systems, ...) and conferences (FICC, CC, SAIC, ...).

9. Principal Publications

Shahnam Mirzaei, Ana C. Cadavid, Vicky Pedone, Werner Horn, Collaborative Interdisciplinary Research Through Projects From Concept To Completion, American Society of Engineering Education (ASEE), June 2018, Salt Lake City, Utah, USA - Best Diversity Paper Award

Uzaif Sharif, Shahnam Mirzaei, High Level Synthesis Implementation of Object Tracking Algorithm on Reconfigurable Hardware, International Symposium on Applied Reconfigurable Computing (ARC), May 2018, Greece

Peter Littlewood, Shahnam Mirzaei, and Krishna Murthy Kattiyani Ramamoorthy, Reconfigurable IP-Based Spectral Interference Canceller, International Symposium on Applied Reconfigurable Computing (ARC), May 2018, Greece

Beulah Preethi Vallur, Shahnam Mirzaei, Shahram Mirzai, and Krishna Murthy Kattiyani Ramamoorthy, Cerebral Blood Flow Monitoring Using IoT Enabled Cloud Computing for mHealth Applications, Future of Information and Communication Conference (FICC), April 2018, Singapore

Peter Littlewood, Shahnam Mirzaei, and Krishna Murthy Kattiyani Ramamoorthy, Modeling of Digital Baseband Interference Canceller Using Hilbert and Fourier Transforms, IEEE 13th International Colloquium on Signal Processing & Its Applications (CSPA), March 2017, Penang, Malaysia

Mehrdad Amanabi, and Shahnam Mirzaei, Design and Implementation of Multi Radio, IoT Enabled Autonomous Greenhouse WSN, IEEE International Symposium on Personal, Indoor and Mobile Radio Communications (PIMRC), Valenica, Spain, September 2016

Alexander Standridge, Calvin Ho, Shahnam Mirzaei, Ramin Roosta, Janarbek Matai, and Ryan Kastner, Design Space Exploration of Scalable Bitcoin Miner on FPGA Using High-Level Synthesis, IEEE International Symposium on Field-Programmable Custom Computing Machine (FCCM), Boston, USA, May 2014

Alexander Standridge, Calvin Ho, Shahnam Mirzaei, and Ramin Roosta, Efficient Bitcoin Miner System Implemented on Zynq 7000 SoC, Xilinx Xcell Journal, Issue 84, July 2013

Deborah Goshorn, Shahnam Mirzaei, Junguk Cho, and Ryan Kastner, Field Programmable Gate Array Implementation of Parts-based Object Detection for Real Time Video Applications, International Conference on Field Programmable Logic and Applications (FPL), Milano, Italy, August 2010

Shahnam Mirzaei, Anup Hosangadi, and Ryan Kastner, Layout Aware Optimization of High Speed Fixed Coefficient FIR Filters for FPGAs, ACM Transactions on Reconfigurable Technology and Systems (IJRC). September 2010

10. Professional development activities in the last six years

Regularly attended professional conferences and presented papers.